

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional Application of )  
Shunpei YAMAZAKI et al. )  
Based on Serial No. 08/142,048 ) Art Unit: 1104  
Filed: December 14, 1994 ) Examiner: C. Chaudhari  
For: LASER PROCESSING METHOD )  
FOR FORMING A FLASH )  
MEMORY, INSULATED GATE )  
SEMICONDUCTOR DEVICE AND )  
METHOD FOR FORMING THE )  
SAME ) Date: December 14, 1994

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

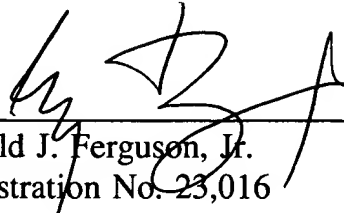
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were cited in Serial No. 08/142,048 filed October 28, 1993, which is the parent of the

subject application.

Respectfully submitted,



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